



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AF *ZDW*

Applicant: Mukesh K Patel
Title: Java Hardware Accelerator Using Microcode Engine

Docket No.: 000006.P001C
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Examiner: Chameli C. Das

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Group Art Unit: 2122

MS After Final

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard and this transmittal document.
- ☒ Response to Final Office Action (25 Pages).
- ☒ Terminal Disclaimer (1 pg.).
- ☒ Statement Under 37 CFR 3.73(b) (1 pg.).
- ☒ Revocation and Power of Attorney Statement under 37 CFR 3.73(b) (2 pgs.).

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 26 day of July, 2005.

Jane E. Sagers

Name

Jane E. Sagers
Signature

(GENERAL)